TalentPros Showcases High-Performance FPGA Prototyping Solutions

at Computex 2026

TalentPros will showcase its latest FPGA prototyping and validation solutions at COMPUTEX 2026, exhibiting in the RISC-V Pavilion from June 2 to June 5.

The company will present its flagship VP1902 prototyping platform, featuring a modular architecture and support for high-speed interfaces. Designed for complex SoC verification, it enables rapid deployment and helps engineering teams accelerate development while reducing time-to-market.

Also on display is the Rapid2 Partition Tool, which enhances multi-FPGA partitioning efficiency through advanced automation and optimization, improving resource utilization and overall system performance.

A range of high-speed daughter cards and peripheral modules will be showcased, supporting PCIe Gen5, PAM4 56G, and QSFP-DD. These solutions are tailored for demanding applications such as AI, networking, and high-performance computing.

With extensive experience in FPGA-based prototyping, TalentPros delivers flexible and customizable solutions to meet the evolving needs of IC design teams.

Visitors are invited to Booth B6 at the RISC-V Pavilion to explore how FPGA prototyping can accelerate next-generation chip development.

Exhibition Information

Event: COMPUTEX 2026

Date: June 2–5, 2026

Venue: Taipei Nangang Exhibition Center, RISC-V Pavilion

Booth: B6

TALENTPROS
FPGA Innovation Partner